INTRODUCING THE L297 STEPPER MOTOR CONTROLLER

The L297 integrates all the control circuitry required to control bipolar and unipolar stepper motors. Used with a dual bridge driver such as the L298 it forms a complete microprocessorto-bipolar stepper motor interface. Unipolar stepper motors can be driven with an L297 plus a guad darlington array. This note describes the operation of the circuit and shows how it is used.

The L297 Stepper Motor Controller is primarily intended for use with an L298 or L293E bridge driver in stepper motor driving applications.

It receives control signals from the system's controller, usually a microcomputer chip, and provides all the necessary drive signals for the power stage. Additionally, it includes two PWM chopper circuits to regulate the current in the motor windings.

With a suitable power actuator the L297 drives two phase bipolar permanent magnet motors, four phase unipolar permanent magnet motors and four phase variable reluctance motors. Moreover, it handles normal, wave drive and half step drive modes. (This is all explained in the section "Stepper Motor Basics").

Two versions of the device are available: the regular

per motor interface.

L297 and a special version called L297A. The L297A incorporates a step pulse doubler and is designed specifically for floppy-disk head positioning applications.

ADVANTAGES

The L297 + driver combination has many advantages: very few components are required (so assembly costs are low, reliability high and little space required), software development is simplified and the burden on the micro is reduced. Further, the choice of a two-chip approach gives a high degree of flexibility - the L298 can be used on its own for DC motors and the L297 can be used with any power stage, including discrete power devices (it provides 20 mA drive for this purpose).



For bipolar motors with winding currents up to 2.5A the L297 should be used with the L298, for winding currents up to 1A the L293E is recommended (the L293 will also be useful if the chopper isn't needed) Higher currents are obtained with power transistors or darlingtons and for unipolar motors a darlington array such as the L7180 is suggested. The block diagram, figure 1, shows a typical system.

Applications of the L297 can be found almost everywhere printers (carriage position, daisy position, paper feed, ribbon feed), typewriters, plotters, numerically controlled machines, robots, floppy disk drives, electronic sewing machines, cash registers, photocopiers, telex machines, electronic carburetors, telecopiers, photographic equipment, paper tape readers, optical character recognisers, electric valves and so on

The L297 is made with SGS' analog/digital compatible 1^2 L technology (like Zodiac) and is assembled in a 20-pin plastic DIP A 5V supply is used and all signal lines are TTL/CMOS compatible or open collector transistors High density is one of the key features of the technology so the L297 die is very compact

THE L298 AND L293E

Since the L297 is normally used with an L298 or L293E bridge driver a brief review of these devices will make the rest of this note easier to follow

The L298 and L293E contain two bridge driver stages, each controlled by two TTL-level logic inputs and a TTL-level enable input in addition, the emitter connections of the lower transistors are brought out to external terminals to allow the connection of current sensing resistors (figure 2)

For the L298 SGS' innovative ion-implanted high voltage/high current technology is used, allowing it to handle effective powers up to 200W (46V supply, 25A per bridge) A separate 5V logic supply input is provided to reduce dissipation and to allow direct connection to the L297 or other control logic

In this note the pins of the L298 are labelled with the pin names of the corresponding L297 terminals to avoid unnecessary confusion

The L298 is supplied in a 15-lead Multiwatt $^{(R)}$ plas tic power package It's smaller brother, the func tionally identical L293E, is packaged in a Powerdip – a copper frame DIP that uses the four center pins to conduct heat to the circuit board copper

Fig 2 - The L298 contains two bridge drivers (four push pull stages) each controlled by two logic inputs and an enable input External emitter connections are provided for current sense resistors. The L293E has external connections for all four emitters



STEPPER MOTOR BASICS

There are two basic types of stepper motor in com mon use permanent magnet and variable reluc tance Permanent magnet motors are divided into bipolar and unipolar types

Bipolar motors

Simplified to the bare essentials, a bipolar per-

manent magnet motor consists of a rotating per manent magnet surrounded by stator poles carrying the windings (figure 3) Bidirectional drive current is used and the motor is stepped by swit ching the windings in sequence

For a motor of this type there are three possible drive sequences

The first is to energize the windings in the sequence AB/CD/BA/DC (BA means that the winding AB is

Fig. 3 – Greatly simplified, a bipolar permanent magnet stepper motor consist of a rotating magnet surrounded by stator poles as shown.



energized but in the opposite sense). This sequence is known as "one phase on" full step or wave drive mode. Only one phase is energized at any given moment (figure 4a).

The second possibility is to energize both phases together, so that the rotor always aligns itself between two pole positions. Called "two-phase-on" full step, this mode is the normal drive sequence for a bipolar motor and gives the highest torque (figure 4b).

The third option is to energize one phase, then two, then one, etc., so that the motor moves in half step increments. This sequence, known as half step mode, halves the effective step angle of the motor but gives a less regular torque (figure 4c).

For rotation in the opposite direction (counterclockwise) the same three sequences are used, except of course that the order is reversed.

As shown in these diagrams the motor would have a step angle of 90° . Real motors have multiple poles to reduce the step angle to a few degrees but the number of windings and the drive sequences are unchanged. A typical bipolar stepper motor is shown in figure 5.

Fig. 4 - The three drive sequences for a two phase bipolar stepper motor. Clockwise rotation is shown.

Fig. 4a – Wave drive (one phase on)



Fig. 4b – Two phase on drive





B.



A٥

N . C

B⁰

5-5952



Fig. 4c - Half step drive











٥4

N . S

B0





85

Fig. 5 - A real motor. Multiple poles are normally employed to reduce the step angle to a practical value. The principle of operation and drive sequences remain the same.



Unipolar motors

A unipolar permanent magnet motor is identical to the bipolar machine described above except that bifilar windings are used to reverse the stator flux, rather than bidirectional drive (figure 6).

This motor is driven in exactly the same way as a bipolar motor except that the bridge drivers are replaced by simple unipolar stages – four darlingtons or a quad darlington array. Clearly, unipolar motors are more expensive because thay have twice as many windings. Moreover, unipolar motors give less torque for a given motor size because the windings are made with thinner wire. In the past unipolar motors were attractive to designers because they simplify the driver stage. Now that monolithic push pull drivers like the L298 are available bipolar motors are becoming more popular.

All permanent magnet motors suffer from the counter EMF generated by the rotor, which limits the rotation speed. When very high slewing speeds are necessary a variable reluctance motor is used.





Variable reluctance motors

A variable reluctance motor has a non-magnetized soft iron rotor with fewer poles than the stator (figure 7). Unipolar drive is used and the motor is stepped by energizing stator pole pairs to align the rotor with the pole pieces of the energized winding.

Once again three different phase sequences can be used. The wave drive sequence is A/C/B/D; two-phase-on is AC/CB/BD/DA and the half step sequence is A/AC/C/BC/B/D/DA. Note that the step angle for the motor shown above is 15° C, not 45° .

As before, practical motors normally employ multiple poles to give a much smaller step angle. This does not, however, affect the principle of operation or the drive sequences.

Fig. 7 – A variable reluctance motor has a soft iron rotor with fewer poles than the stator. The step angle is 15° for this motor.



GENERATING THE PHASE SEQUENCES

The heart of the L297 block diagram, figure 8, is a block called the translator which generates suitable phase sequences for half step, one-phase-on full step and two-phase-on full step operation. This block is controlled by two mode inputs — direction (CW/CCW) and HALF/FULL — and a step clock which advances the translator from one step to the next.

Four outputs are provided by the translator for subsequent processing by the output logic block which implements the inhibit and chopper functions.

Internally the translator consists of a 3-bit counter plus some combinational logic which generates a basic eight-step gray code sequence as shown in figure 9. All three drive sequences can be generated easily from this master sequence. This state sequence corresponds directly to half step mode, selected by a high level on the HALF/FULL input. The output waveforms for this sequence are shown in figure 10.

Note that two other signals, $\overline{INH1}$ and $\overline{INH2}$ are generated in this sequence. The purpose of these signals is explained a little further on.

The full step modes are both obtained by skipping alternate states in the eight-step sequence. What happens is that the step clock bypasses the first stage of the 3-bit counter in the translator. The least significant bit of this counter is not affected therefore the sequence generated depends on the state of the translator when full step mode is selected (the HALF/FULL input brought low).

If full step mode is selected when the translator is at any odd-numbered state we get the two-phaseon full step sequence shown in figure 11.

By contrast, one-phase-on full step mode is obtained by selecting full step mode when the translator is at an even-numbered state (figure 12).





Fig. 9 - The eight step master sequence of the translator. This corresponds to half step mode Clockwise rotation is indicated.



Fig. 10 - The output waveforms corresponding to the half step sequence. The chopper action in not shown.



Fig. 11 - State sequence and output waveforms for the two phase on sequence. INH1 and INH2 remain high throughout.



Fig. 12 - State sequence and output waveforms for wave drive (one phase on).



INH1 AND INH2

In half step and one-phase-on full step modes two other signals are generated: INH1 and INH2. These are inhibit signals which are coupled to the L298's enable inputs and serve to speed the current decay when a winding is switched off.

Since both windings are energized continuously in two-phase-on full step mode no winding is ever switched off and these signals are not generated.

To see what these signals do let's look at one half of the L298 connected to the first phase of a twophase bipolar motor (figure 13). Remember that the L298's A and B inputs determine which transistor in each push pull pair will be on. INH1, on the other hand, turns off all four transistors.

Assume that A is high, B low and current flowing through Q1, Q4 and the motor winding. If A is now brought low the current would recirculate through D2, Q4 and R_s , giving a slow decay and in-

creased dissipation in R_s. If, on the other hand, A is brought low and INH1 is activated, all four transistors are turned off. The current recirculates in this case from ground to V_s via D2 and D3, giving a faster decay thus allowing faster operation of the motor. Also, since the recirculation current does not flow through R_s, a less expensive resistor can be used.

Exactly the same thing happens with the second winding, the other half of the L298 and the signals C, D and $\overline{INH2}$.

The $\overline{\text{INH1}}$ and $\overline{\text{INH2}}$ signals are generated by OR functions:

 $A + B = \overline{INH1}$ $C + D = \overline{INH2}$

However, the output logic is more complex because the inhibit lines are also used by the chopper, as we will see further on. Fig. 13 – When a winding is switched off the inhibit input is activated to speed current decay. If this were not done the current would recirculate through D2 and Q4 in this example. Dissipation in R_s is also reduced.



OTHER SIGNALS

Two other signals are connected to the translator block: the RESET input and the HOME output.

RESET is an asynchronous reset input which restores the translator block to the home position (state 1, ABCD = 0101). The HOME output (open collector) signals this condition and is intended to be ANDed with the output of a mechanical home position sensor.

Finally, there is an ENABLE input connected to the output logic. A low level on this input brings INH1, INH2, A, B, C and D low. This input is useful to disable the motor driver when the system is initialized.

LOAD CURRENT REGULATION

Some form of load current control is essential to obtain good speed and torque characteristics. There are several ways in which this can be done — switching the supply between two voltages, pulse rate modulation chopping or pulse width modulation chopping.

The L297 provides load current control in the form of two PWM choppers, one for each phase of a bipolar motor or one for each pair of windings for a unipolar motor. (In a unipolar motor the A and B windings are never energized together so thay can share a chopper; the same applies to C and D).

Each chopper consists of a comparator, a flip flop and an external sensing resistor. A common onchip oscillator supplies pulses at the chopper rate to both choppers. In each chopper (figure 14) the flip flop is set by each pulse from the oscillator, enabling the output and allowing the load current to increase. As it increases the voltage across the sensing resistor increases, and when this voltage reaches V_{ref} the flip flop is reset, disabling the output until the next oscillator pulse arrives. The output of this circuit (the flip flop's Q output) is therefore a constant rate PWM signal. Note that V_{ref} determines the peak load current.

Fig. 14 – Each chopper circuit consists of a comparator, flip flop and external sense resistor. A common oscillator clocks both circuits.



PHASE CHOPPING AND INHIBIT CHOPPING

The chopper can act on either "the phase lines (ABCD) or on the inhibit lines INH1 and INH2. An input named CONTROL decides which. Inhibit chopping is used for unipolar motors but you can choose between phase chopping and inhibit chopping for bipolar motors. The reasons for this choice are best explained with another example.

First let's examine the situation when the phase lines are chopped.

As before, we are driving a two phase bipolar motor and A is high, B low (figure 15). Current therefore flows through Q1, winding, Q4 and R_s . When the voltage across R_s reaches V_{ref} the chopper brings B high to switch off the winding.

Fig. 15 - Phase chopping. In this example the current X is interrupted by activating B, giving the recirculation path Y. The alternative, de-activating A, would give the recirculation path Z, increasing dissipation in R_s .



The energy stored in the winding is dissipated by current recirculating through $\Omega1$ and D3. Current decay through this path is rather slow because the voltage on the winding is low ($V_{CEsat} Q_1 + V_{D3}$) (figure 16).

is to avoid the current decaying through $\rm R_s.$ Since the current recirculates in the upper half of the bridge, current only flows in the sensing resistor when the winding is driven. Less power is therefore dissipated in $\rm R_s$ and we can get away with a cheaper resistor.

Why is B pulled high, why push A low? The reason





This explains why phase chopping is not suitable for unipolar motors: when the A winding is driven the chopper acts on the B winding. Clearly, this is no use at all for a variable reluctance motor and would be slow and inefficient for a bifilar wound permanent magnet motor. The alternative is to tie the CONTROL input to ground so that the chopper acts on $\overline{INH1}$ and $\overline{INH2}$. Looking at the same example, A is high and B low. Q1 and Q4 are therefore conducting and current flows through Q1, the winding, Q4 and R_s (figure 17).





In this case when the voltage across R_s reaches V_{REF} the chopper flip flop is reset and INH1 activated (brought low). INH1, remember, turns off all four transistors therefore the current recirculates from ground, through D2, the winding and D3 to V_s. Discharged across the supply, which can be up to 46V, the current decays very rapidly (figure 18).

The usefulness of this second faster decay option is fairly obvious; it allows fast operation with bipolar motors and it is the only choice for unipolar motors. But why do we offer the slower alternative, phase chopping?

The answer is that we might be obliged to use a low chopper rate with a motor that does not store much energy in the windings. If the decay is very fast the average motor current may be too low to give an useful torque. Low chopper rates may, for example, be imposed if there is a larger motor in the same system. To avoid switching noise on the ground plane all drivers should be synchronized and the chopper rate is therefore determined by the largest motor in the system.

Multiple L297s are synchronised easily using the SYNC pin. This pin is the squarewave output of the on-chip oscillator and the clock input for the choppers. The first L297 is fitted with the oscillator components and outputs a squarewave signal

on this pin (figure 19). Subsequent L297s do not need the oscillator components and use SYNC as a clock input. An external clock may also be injected at this terminal if an L297 must be synchronized to other system components.

Fig. 18 – Inhibit chopper waveforms. Winding AB is energized and CONTROL is low.



the winding is discharged and therefore speed the current decay.

In all applications where the choppers are not used it is important to remember that the sense inputs must be grounded and V_{REF} connected either to V_s or any potential between V_s and ground.

The chopper oscillator frequency is determined by the RC network on pin 16. The frequency is roughly 1/0.7 RC and R must be more than 10K Ω . When the L297A's pulse doubler is used, the delay time is determined by the network R_d C_d and is approximately 0.75 R_d C_d. R_d should be in the range 3 k Ω - 100 k Ω (figure 23).

Fig. 23 – The clock pulse doubler inserts a ghost pulse τ_0 seconds after the input clock pulse. $R_d C_d$ is closen to give a delay of approximately half the input clock period.



PIN FUNCTIONS - L297

| N° | NAME | FUNCTION | | | |
|----|--|---|--|--|--|
| 1 | SYNC | Output of the on-chip chopper oscillator. The SYNC connections of all L297s to be synchronized are connected together and the oscillator components are omitted on all but one. If an external clock source is used it is injected at this terminal. | | | |
| 2 | GND | Ground connection. | | | |
| 3 | НОМЕ | Open collector output that indicates when the L297 is in its initial state (ABCD = 0101). The transistor is open when this signal is active. | | | |
| 4 | Α | Motor phase A drive signal for power stage. | | | |
| 5 | ĪNH1 | Active low inhibit control for driver stages of A and B phases. When a bipolar bridge is used this signal can be used to ensure fast decay of load current when a winding is de-energized. Also used by chopper to regulate load current if CONTROL input is low. | | | |
| 6 | B. Start B | Motor phase B drive signal for power stage. | | | |

PIN FUNCTIONS - L297 (continued)

| N° | NAME | FUNCTION Motor phase C drive signal for power stage. | | | |
|----|-------------------|---|--|--|--|
| 7 | C | | | | |
| 8 | INH2 | Active low inhibit control for drive stages of C and D phases. Same functions as INH1. | | | |
| 9 | D | Motor phase D drive signal for power stage. | | | |
| 10 | ENABLE | Chip enable input. When low (inactive) INH1, INH2, A, B, C and D are brought low. | | | |
| 11 | CONTROL | Control input that defines action of chopper. When low chopper acts on INH1 and INH2; when high chopper acts on phase lines ABCD. | | | |
| 12 | V _s | 5V supply input. | | | |
| 13 | SENS ₂ | Input for load current sense voltage from power stages of phases C and D. | | | |
| 14 | SENS1 | Input for load current sense voltage from power stages of phases A and B. | | | |
| 15 | V _{ref} | Reference voltage for chopper circuit. A voltage applied to this pin determines the peak load current. | | | |
| 16 | OSC | An RC network (R to V _{CC} , C to ground) connected to this terminal determines the chopper rate. This terminal is connected to ground on all but one device in synchronized multi - L297 configurations. $f \cong 1/0.69$ RC, R > 10 k Ω . | | | |
| 17 | cw/ccw | Clockwise/counterclockwise direction control input. Physical direction of motor rotation also depends on connection of windings. Synchronized internally therefore direction can be changed at any time. | | | |
| 18 | CLOCK | Step clock. An active low pulse on this input advances the motor one increment. The step occurs on the rising edge of this signal. | | | |
| 19 | HALF/FULL | Half/full step select input. When high selects half step operation; when low selects full step operation. One- phase-on full step mode is obtained by selecting FULL when the L297's translator is at an even-numbered state. Two-phase-on full step mode is set by selecting FULL when the translator is at an odd numbered position. (The home position is designated state 1). | | | |

PIN FUNCTIONS – L297(continued)

| N° | NAME | | | FUNCTION | | |
|----|------------|--|-------------------------------|---|------------------------------------|-------------|
| | | | | | | |
| 20 | RESET | | Reset input. A the translator | n active low pulse or to the home position | n this input rest (state 1, ABC | ores D = |
| | tel contra | Na kaominina dia kaominina d | 0101). | · · · · · · · · · · · · · · · · · · · | | |

PIN FUNCTIONS - L297A

Pin function of the L297A are identical to those of the L297 except for pins 1 and 11.

| N° | NAME | FUNCTIONS | | | | |
|----|---------|--|--|--|--|--|
| 1 | DOUBLER | An RC network connected to this pin determines the delay between an input clock pulse and the corresponding ghost pulse. | | | | |
| 11 | DIR-MEM | Direction Memory. Inverted output of the direction flip flop. Open collector output. | | | | |

Fig. 24 – Connection diagrams

| CV/010 | <u>ا. </u> | <u> </u> | | | | h | |
|--------|---|----------|------------------|---------|---|-------|------------------|
| STINC | 4' | νų | RESEI | DOOBLER | 4 ' | ۲°۲ | RESEI |
| GND | 2 | 19 | HALF/FULL | GND | 2 | 19 | HALF/FULL |
| HOME | [3 | 18 | CLOCK | номе | 3 | 18 | CLOCK |
| A | [4 | 17 | cw/ccw | Α | [4 · · · · · · · · · · · · · · · · · · | 17 | cw/ccw |
| INH 1 | 5 L297 | 16 | osc | INH 1 | 5 L297A | 16 | osc |
| в | 6 | 15 | V _{ref} | В | 6 | 15 | V _{ref} |
| с | 7 | 14] | SENS 1 | c | 7 | 14 | SENS 1 |
| INH 2 | 8 | 13 | SENS 2 | INH 2 | 8 | 13 | SENS 2 |
| D | e] | 12 | ٧ _S | D | 9 | 1 2 | ٧ _S |
| ENABLE | 10 | וי | CONTROL | ENABLE | 10 | ןיי þ | DIR-MEM |
| | <u>ــــــــــــــــــــــــــــــــــــ</u> | -5839 | | | <u>د</u> | -5840 | |